

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((state\$1 adj1 machine\$1) or finite-state-machine or fsm) SAME (cml or (differential NEAR2 (circuit or buffer or driver or receiver)) or ((common or current) NEAR2 logic) or lvd\$1) SAME load SAME transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/21 09:45
L2	37	((state\$1 adj1 machine\$1) or finite-state-machine or fsm) and (cml or (differential NEAR2 (circuit or buffer or driver or receiver)) or ((common or current) NEAR2 logic) or lvd\$1) SAME load SAME transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/21 14:24
L3	54	((state\$1 adj1 machine\$1) or finite-state-machine or fsm) and (cml or (differential NEAR2 (circuit or buffer or driver or receiver)) or ((common or current) NEAR2 logic) or lvd\$1) SAME load SAME transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 10:48
L4	355	((state\$1 adj1 machine\$1) or finite-state-machine or fsm) and (cml or (differential NEAR2 (circuit or buffer or driver or receiver)) or ((common or current) NEAR2 logic) or lvd\$1) SAME transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 14:11
L5	6	4 and (inductor\$1 NEAR3 load\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 14:15
L6	114	4 and ((active\$1 or transistor\$1) NEAR3 load\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 14:23
L7	275	("326"/\$.ccls. or "327".\$ccls. or "330"/\$.ccls.) and ((state\$1 adj1 machine\$1) or finite-state-machine or fsm) and (cml or (differential NEAR2 (circuit or buffer or driver or receiver)) or ((common or current) NEAR2 logic) or lvd\$1) and transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/21 14:39

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L9	57	7 and induct\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/21 14:27
L10	3	7 and ((cml or (differential NEAR2 (circuit or buffer or driver or receiver)) or ((common or current) NEAR2 logic) or lvd\$1) SAME ((transistor or active or induct\$3) NEAR2 load))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/21 14:41

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L1	0	((state\$1 adj1 machine\$1) or finite-state-machine or fsm) SAME (cml or (differential NEAR2 (circuit or buffer or driver or receiver)) or ((common or current) NEAR2 logic) or lvd\$1) SAME (load\$1 NEAR3 (transistor or active or induct\$3))).clm.	US-PGPUB	OR	ON	2006/11/21 14:45
L2	2	((state\$1 adj1 machine\$1) or finite-state-machine or fsm) and (cml or (differential NEAR2 (circuit or buffer or driver or receiver)) or ((common or current) NEAR2 logic) or lvd\$1) and (load\$1 NEAR3 (transistor or active or induct\$3))).clm.	US-PGPUB	OR	ON	2006/11/21 14:45